tScan16[™] - High-Density T1 E1 Analysis Tool (16 T1 E1 Rx Only Ports)



818 West Diamond Avenue - Third Floor, Gaithersburg, MD 20878 Phone: (301) 670-4784 Fax: (301) 670-9187 Email: <u>info@gl.com</u> Website: <u>https://www.gl.com</u>

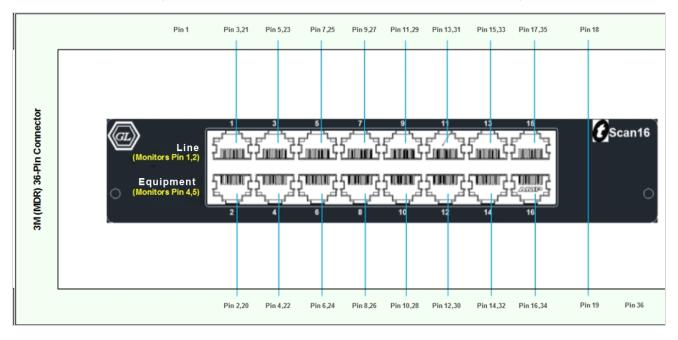
PCIe based tScan16[™] T1 E1 Board

16 Dual RJ45c Jacks Breakout Box





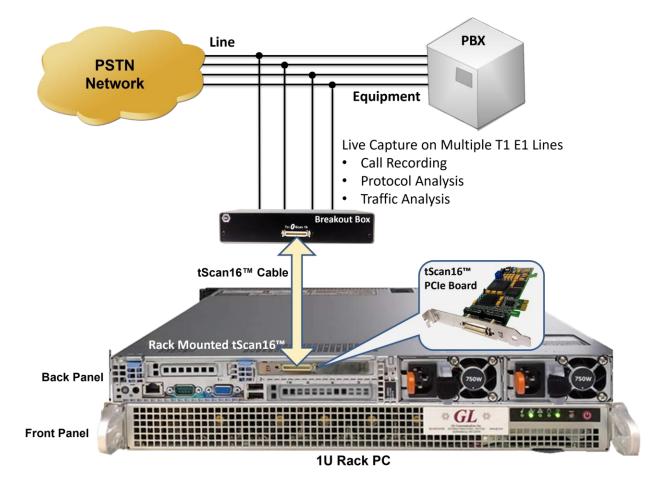
tScan16[™] Breakout Box- Pinout (MDR 36-pin Connector)



- The tScan16[™] Breakout Box is used to receive the T1/E1 traffic on 16 ports for tScan16[™] application
- Consists of 8 pairs of Line and Equipment RJ45c ports



Working Principle





Why is this tool superior?

- High Density and High Speed The boards (with Direct Memory Access) are significantly faster and significantly more efficient
- Supports high performance voice and data applications
- PCI Express x1 Lane/Board
- Reduces hardware costs and power consumption



Main Features

- Software selectable 16 Rx Only T1 or E1 interfaces
- Convenient High-density Cabling
- Monitor T1/E1 line conditions such as frame errors, bipolar violations, alarms, frequency, power level, and clock (or frame/bit) slips
- Analysis of ISDN, SS7, Frame Relay, Multilink Frame Relay, PPP and Multilink PPP, HDLC, and many more protocols
- Comprehensive analysis of Voice, Data, Fax, Protocols, and Digital signals, including Echo and Voice Quality testing
- Call Recording, Analysis, and Monitoring for hundreds to thousands of calls in one platform
- The data (Signaling, and Traffic Call Data Records) collected at probe-level are stored into a relational database (Oracle) using Open Database Connectivity
- With the use of NetSurveyorWeb[™] application, the real-time and historic call data records can be accessed using simple web browser interface for remote or local monitoring

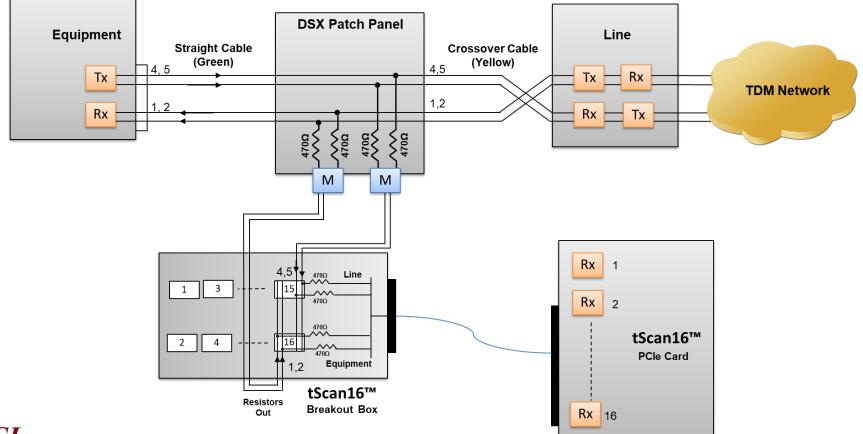


Quad and Octal Cards vs tScan16[™]Cards

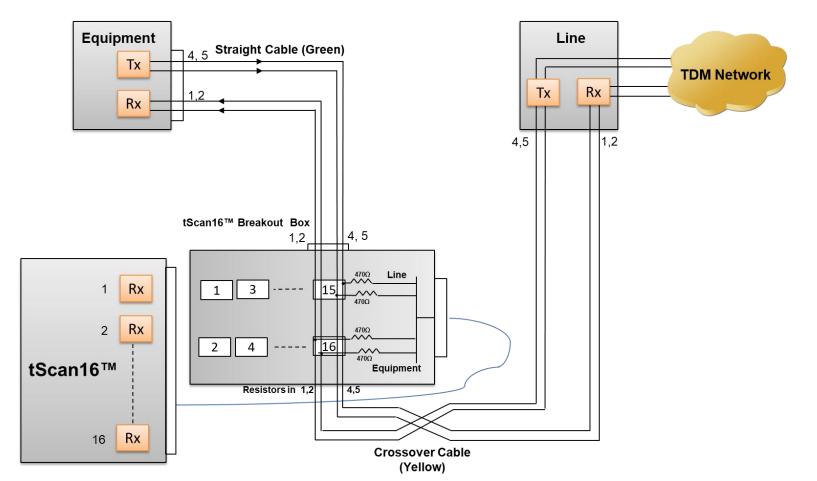
Feature	Quad, Octal T1 E1 Boards	tScan16™ T1/E1 Boards
Number of Ports	4, 8	16 (Rx only) 31x16 = 496 timeslots for E1 24x16 = 384 timeslots for T1
Board Height:	Standard PCIe board	Standard PCIe board or 2U
PC Bus:	PCle v1.1 x1	PCle v1.1 x1
Adapter Board:	Quad: none; Octal: 4 port daughterboard	16 port RJ45 breakout board with MDR 36-Pin Connector (3M Mini D Ribbon cable) to interface with main board
T1/E1 Connectors:	RJ-45	RJ-45 with Inline Monitoring T1/E1
T1/E1 Termination modes:	Terminate, Bridge, Monitor	Terminate and Monitor (on main board)
T1/E1 interface modes:	Normal Mode, Cross-port Through Mode	Receive Only into main board. Breakout adaptor board connects equipment side and line side using RJ45 connector.



tScan[™] with DSX1 Patch Panel



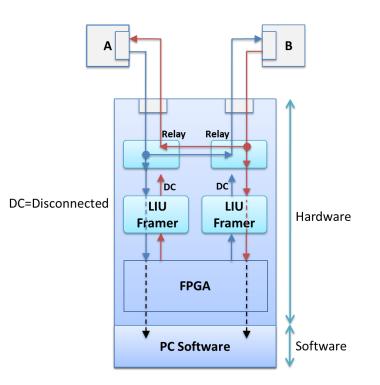
tScan[™] without DSX1 Patch Panel



Communications

Cross-port Through Loopback

- Allows monitoring T1 E1 lines in-line while still being protected from loss of power to the board
- It is implemented entirely thru relays and eliminates complex cabling
- The signal received on Port 2 is transmitted out onto
 Port 1





T1 E1 Basic Software

- T1 E1 Basic Software
 - Monitoring Options
 - Intrusive Testing
 - > Windows Client /
 - Server
 - Remote access to
 - T1/E1 server
 - Clients Python and

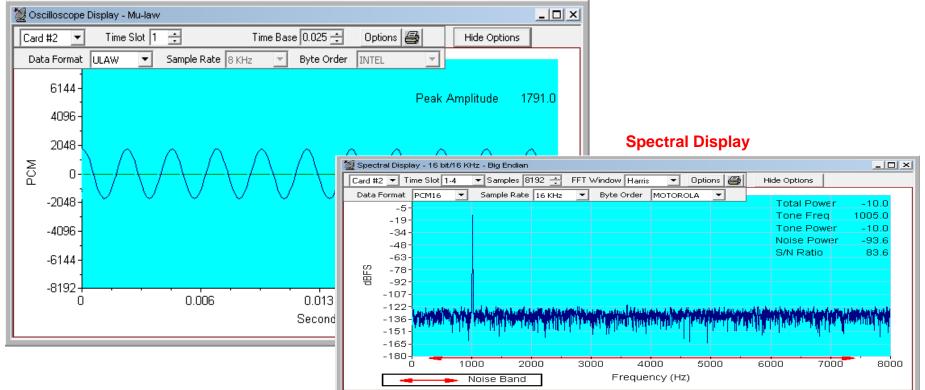
- Monitoring Features
 - Byte Values & Binary Byte Values
 - Signaling bits, Power Level, DC Offset, & Frequency
 - Multi-frames, and Real-time Multi-frames
 - T1/E1 Data as Real-time Bitmap
 - Timeslot Window
 - ASCII Timeslot Display
 - Oscilloscope & Power Spectral
 - Active Voice Level

- Intrusive Testing
 - Bit Error Rate Test
 - Enhanced BERT

Java

Monitoring Features

Oscilloscope Display





Client Server

 Allow the user (with an appropriate client) to operate analyzers remotely, write scripts for automation, or provide multi client connectivity to a single T1 E1 analyzer

💑 E1_Regressiontest.gls - GLClient		×				
Eile Edit Yiew Connect Script Log User Help						
📘 🗅 🚅 🔚 X 🖻 🛍 🚑 🚮	路 🗅 🛩 🖃 🛤 🐚 📴 🗛 🚦 🌹					
get board count;						
board_count=2						
get response; response = 500.0						
go 0,0,0,0 #1;						
OK						
get signaling bits #2:115;						
#2:1.sig_bits=0,0,0,0						
#2:2.sig_bits=0,0,0,0 #2:3.sig_bits=0,0,0,0						
#2:4.sig_bits=0,0,0,0						
#2:5.sig_bits=0,0,0,0						
#2:6.sig_bits=0,0,0,0		-				
W setting both the cards to cas r	node to get all four signaling bits					
//getting the signaling bits trans		_				
//cross connect card 1 and 2						
go 0,0,0,0 #1;		_				
get signaling bits #2:115;	of signaling bits as mentioned before for time slots 1 to 15 only					
go 0,0,0,1 #1;						
get signaling bits #2:115;	Untitled - GLServer	×				
wait 2000;	Eile Edit View Setup Help					
go 0,0,1,0 #1;	D 🖆 🖬 👗 🖻 🛍 👫 🏞 🎒 😵					
get signaling bits #2:115; wait 2000;	Connected: client #404 at 192.168.1.63					
go 0,0,1,0 #1;	404: set rx interface terminate #*;					
get signaling bits #2:115;	404: set signaling mode cas #*;					
Ready	404: set crc4 on#*; 404: set tx clock source internal #*;					
	404: set outward driver loopback off #*;					
	404: get tx clock source #*;					
	404: get outward driver loopback #*;					
	404: get rx line frequency #*;					
	404: get rx line level #*; 404: get all alarms #*;					
	404: get board count;					
	404: get response;					
	404: go 0,0,0,0 #1;					
	404: get signaling bits #2:115;					
	404: go 0,0,1,0 #1; 404: get signaling bits #2:115;					
	404. get signaling bits #2.1.15, 404: go 0,0,1,0 #1;					
	404: get signaling bits #2:115;					
		-				
	Ready	//.				



T1 E1 Special Applications

- Protocol Analysis
 - ISDN, HDLC, SS7, Frame Relay, TRAU, CDMA, DCME, T1 Facility
 Data Link
 - E1 Maintenance Data Link, UMTS,
 PPP, ATM, GSM, V5.x, GPRS,
 GR303, SS1, Signaling Bit Transitions •
- Captured Dialled Digits
- Realtime Strip Chart
- Realtime Multichannel Audio Bridge
- Capture, Analysis, & Emulation
 - Call Capture and Analysis (CCA)
 - Multiple Call Capture and Analysis

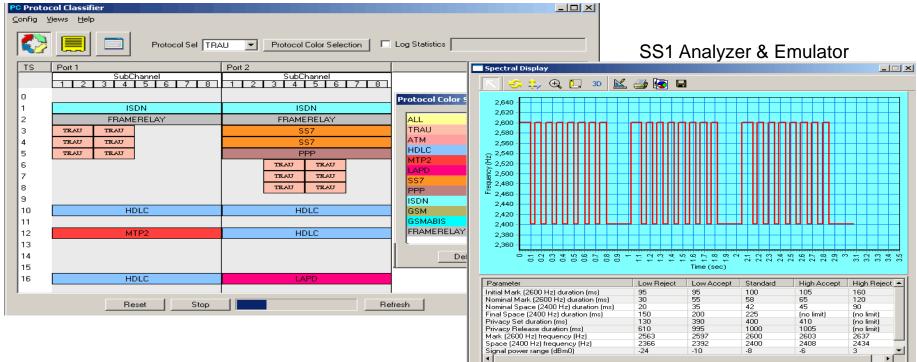
- Voice Band Analysis Software
 - Call Data Records (CDR)
 - Voice Band Analyzer (VBA)
 - Fax Emulation and Analysis
- Fax Simulator
 - Fax Analysis using GLInsight ™ or FaxScan™
- Echo Cancellation Testing / Compliance
 - Manual
 - Semi-automated
 - Automated
- WCS Modules
 - Transmission/reception of files/digits
 - Multi-channel BERT
 - > DSP operations, Dynamic DSP capability
 - SA Bits/ FDL/ HDLC/ TRAU/ MC-MLPPP/ SS7/ ISDN / ML Frame Relay

- Protocol Identifier
- Multi-Channel BERT
- Multiplex / Demultiplex Software
- Network Surveillance

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Protocol Identifier and Analyzer

Protocol Identifier





Call Capture and Analysis

Call Capture & Analysis

	-		indiyolo								
Multiple Call Capture - UsbE1	card #	*1 and #2									
File Capture Settings											
Capture Directory											
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Capture File #1				— TS Displa	av —						
Dec10W01.000		_		1 🗄	Ĩ		I	Multiple Call Capture & A	nalysis		
Bytes Captured: 17024	Multi C	all Capture	for Manual - Un	titled						_	
	File E	dit Trigger	Options Process								
Capture File #2	CC No	Capture N	lame West(Port)) East(Port)	Timeslots		Storage Location	1	Trigger Optio	n Action	
Dec10E01.000	1	CCA1	l 1	2	0-23	}	C:\Program File	es\GL Communications Inc\Dual Ultra HD T1 Analy:	er Edit	Abor	
	2	CCA2	2 1	2	0-23	3	C:\Program File	es\GL Communications Inc\Dual Ultra HD T1 Analy:	er Edit	Abor	:
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	4	CCA4	1 1	2	0-23	}	C:\Program File	es\GL Communications Inc\Dual Ultra HD T1 Analy;	er <u>Edit</u>	Abor	
Signaling File:Dec1001.0											
- Timeslot Activity	TS	TS Status		Wes	t Filename	Bytes	Captured(West)	East File	ame 🛛 🛛 Bytes C	aptured(East)	
		Capturing	C:\Program Files\@				742224	C:\Program Files\GL Communications Inc\Dual U		742224	
01 02 03 04 05 06 07 0 16 17 18 19 20 21 22 23 2	1		C:\Program Files\(C:\Program Files\(742224 742224	C:\Program Files\GL Communications Inc\Dual U C:\Program Files\GL Communications Inc\Dual U		742224	
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		Details /	Timeslots Map /	/							



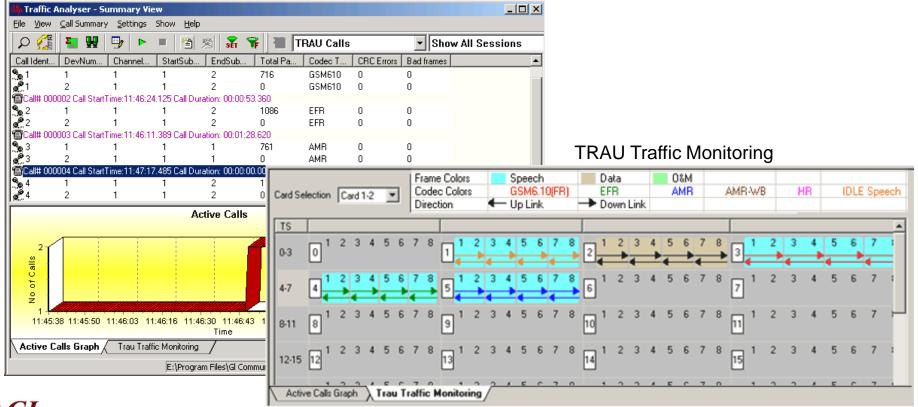
Protocol Analysis

PPP Protocol Analysis

PPP Protocol Analysis PPP	
<u>File View Capture Statistics Database Configure H</u> elp	
≤ ▲ 4 0 4 1 0 0 1 1 0 1 1 1 1	
	or PPP Laye LCP Code IPCP Code BCF
	Link Control Echo-Request
2 1-31 1 00:00:00.000625 14	Link Control Echo-Reply
2 1-31 2 00:00:00.088625 14	Link Control Echo-Request
✓ 1 1-31 3 00:00:00.092000 14	Link Control Echo-Reply PPP Packet Data Analysis
√ 1 1-31 4 00:00:09.993996 14	M. Traffic Analyzer - Summary View
✓ 2 1-31 5 00:00:09.994625 14	File weat Summary Settings Help
✓ 2 1-31 6 00:00:10.082625 14	🖉 🔎 🍓 🕎 🕨 🗉 🖄 🛣 🚏 📲 Sip Calls 💿 Show All Sessions
	Call Summary Registraton Summary Alert Summary
Card1 TimeSlots=1-31 Frame=0 at 00:00:00.000000 OK Len=1 HDLC Frame Data + FCS ========== PPP Link Layer Address = 111111 Ctl = 000000 Protocol = 110000 ====================================	Call # SSRC Payload Packet Received Conversal M05/R Listening Discard. Packets. Packets Duplicate Packets Out Of Packets Average Packets Average
Hex Dump of the Frame Data +	

Protocol Analysis (Contd.)

TRAU Packet Data Analysis - Active Calls Graphs



Thank you

